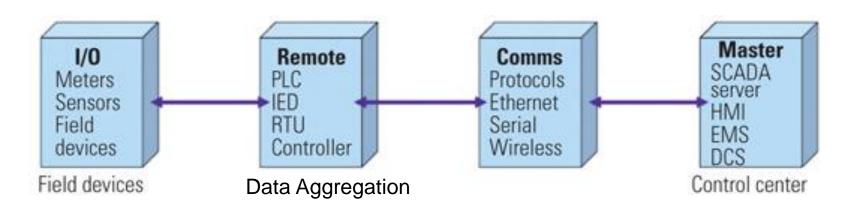




Introduction



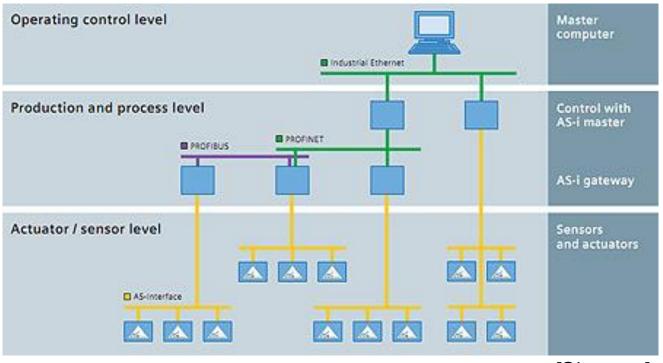
- Industrial Control Systems (ICS) are used to monitor and control industrial facilities and processes:
 - Power Grid: generation, distribution, load balancing and billing
 - Chemical and Nuclear Plant: control of safety critical processes.
 - Gas and Water Facilities: collect measurements from PLC/sensors and issue commands to actuators.





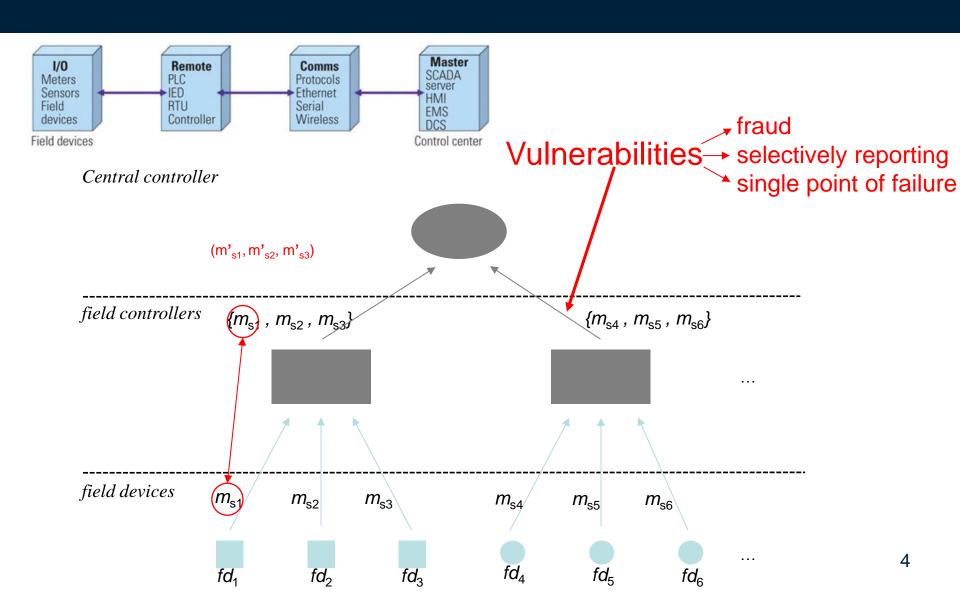
An Example ICS Architecture

- Master ensures data exchange with the slaves (field controller) by means of cyclic polling.
- Data collected at the field controller can be aggregated.





Integrity of Sensor Data





Security Requirements

- **Data Integrity** the measurements on the field devices must reflect the current state of the instruments in the plant.
 - modification and tampering.
- Data Origin Authentication important to ensure that measurements are taken using the designated field devices.
 - **x** spoofing
- **Secure Data Aggregation** though data are aggregated to save bandwidth, the central controller (Back End Master) must have the ability to check the integrity and data origin.
 - integrity data origin



Background: Chameleon Hashing

Chameleon Hashing

- Hash function with a trapdoor for finding collusion.
- Associated with a pair of public-private key.
- Private-key serves as the trapdoor.

Properties

- Chameleon Hash Value [CHV] = CHA_HASH(y, m, r).
- given trapdoor x, find a collision [m', r'] where m' ≠ m and r' ≠ r.
- Hence $[CHV] = CHA_HASH(y, m', r')$.

Chameleon Signature

 Apply traditional signature, e.g., DSA, RSA, ECC to Chameleon Hash.



System Setup

Field Devices



Trapdoor Hash Key (x)



Trapdoor Chameleon Hash Function



Device ID (Id_{fd})

Field Controllers



Chameleon Hash Key (y)



Chameleon Hash Function Back-end





Chameleon Hash Function

Secure Channel

Secure Channel



Chameleon Hash Key

Key Generation

- Krawczyk and Rabin's discrete logarithm construction
 - Two primes p and q are randomly generated such that p = kq+1 where q is a large prime factor.
- An element g of order q in \mathbb{Z}_p^* is chosen so that the private key, $x \in \mathbb{Z}_p^*$. The public-key, y is generated as

$$y = g^x \mod p$$



Generation of Chameleon Hash

• Given a message $m \in \mathbb{Z}_p^*$, choose a random value $r \in \mathbb{Z}_p^*$, the Chameleon Hash denoted as CHV can be computed as:

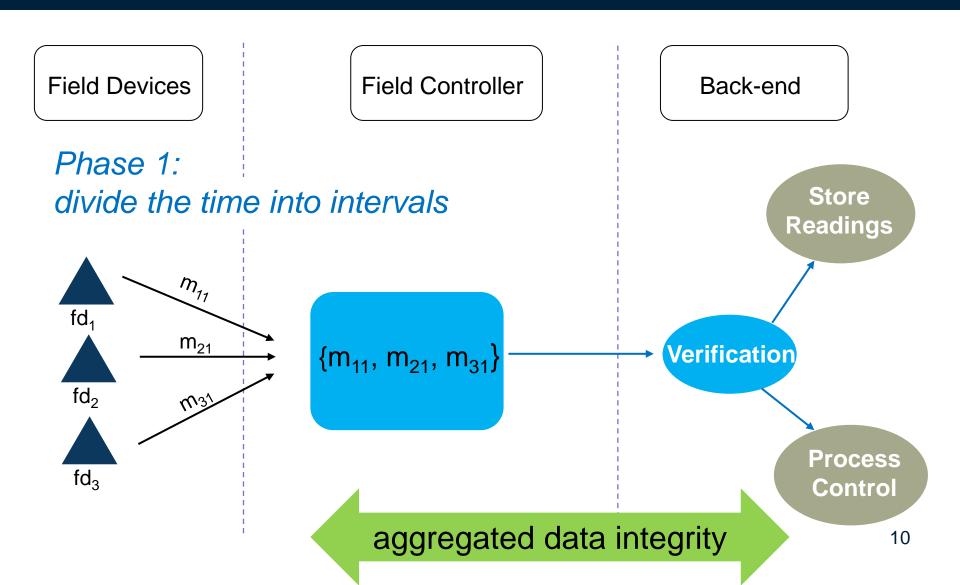
$$CHA_Hash(m,r) = g^m y^r \mod p$$

• Only the field devices have the ability to produce the same Chameleon Hash using a different message, m' such that $CHA_Hash(m,r) = CHA_HASH(m',r')$ by solving r'

$$m + xr = m' + xr' \mod p$$

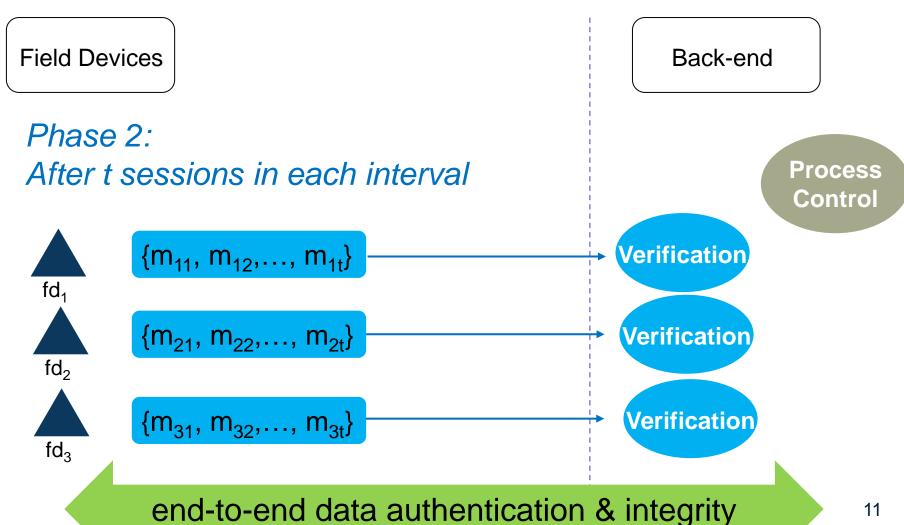


Protocol Overview



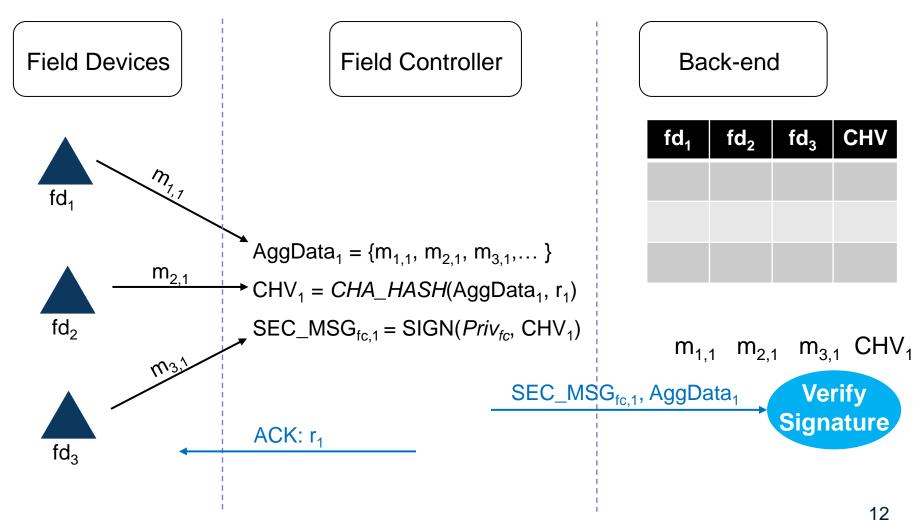


Protocol Overview





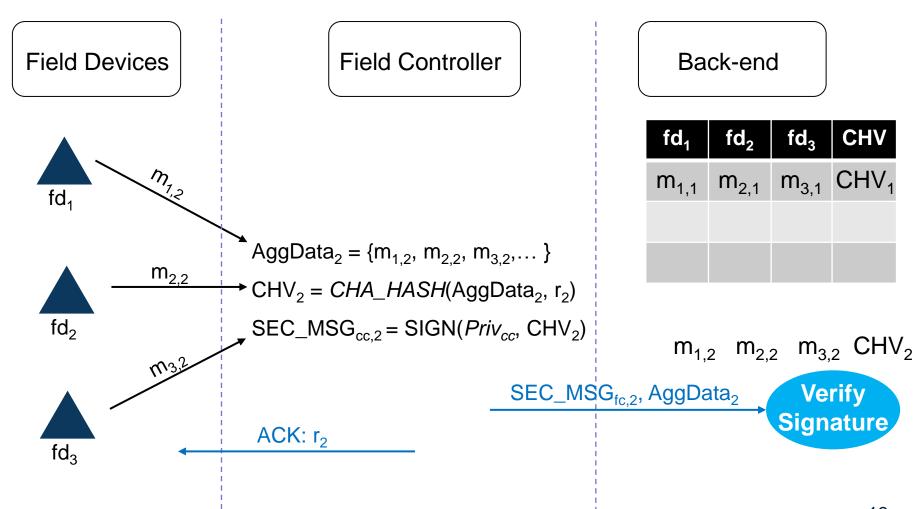
Secure End-to-End **Data Aggregation**



Phase 1: interval 1:Session 1



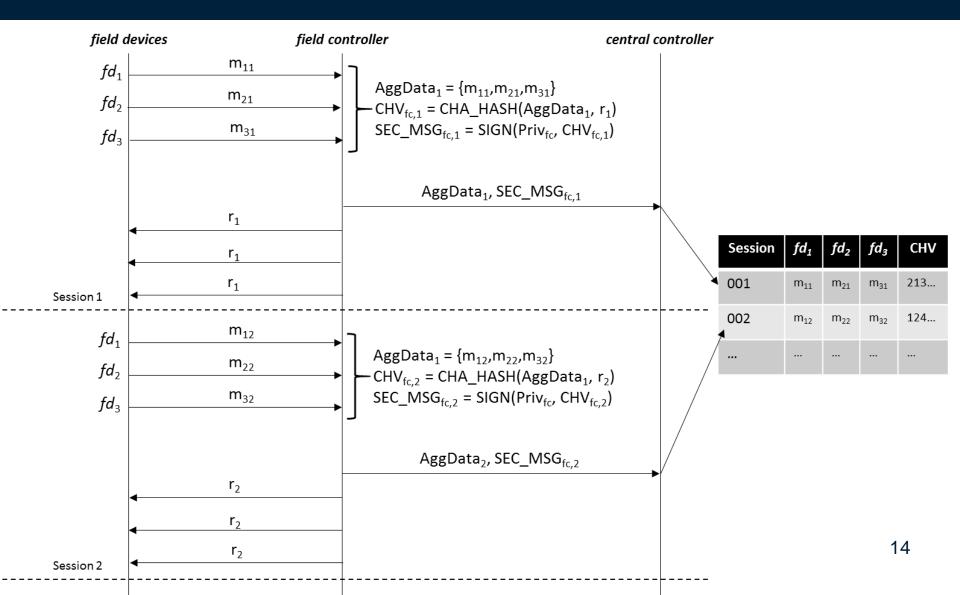
Secure End-to-End Data Aggregation



Phase 1: interval 1: Session 2



Phase 1: Protocol Summary





Phase 2: E2E Integrity Verification

Transmission of Evidence

- Time is divided into intervals, where each interval consists of t sessions.
- At the end of each interval, field devices choose an r_v where $1 \le v \le t$, so that

CHA_HASH
$$(m'_i, r'_i)$$
 = CHA_HASH $(AggData_v, r_v)$

• m' denotes all the readings recorded by the field device i in the interval $\{Id_{fd.i}, m_{i.1}, m_{i.2}, ..., m_{i.t}\}$

Delayed-Integrity-Verification

Transmission of Evidence

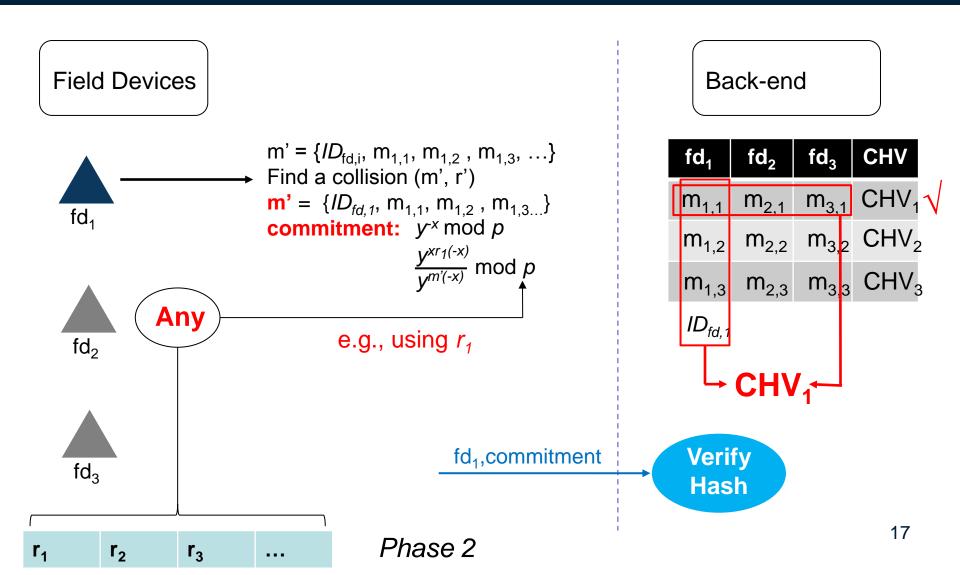
• To verify this, we need to solve r'_{i}

$$r'_i \mod p = (AggData_v + xr_v - m')x^{-1} \mod p$$

 However, field devices do not know AggData_v (sent by the field controller). Instead they can compute a *commitment* that allows the back-end to verify integrity and authenticity.



Delayed-Integrity-Verification



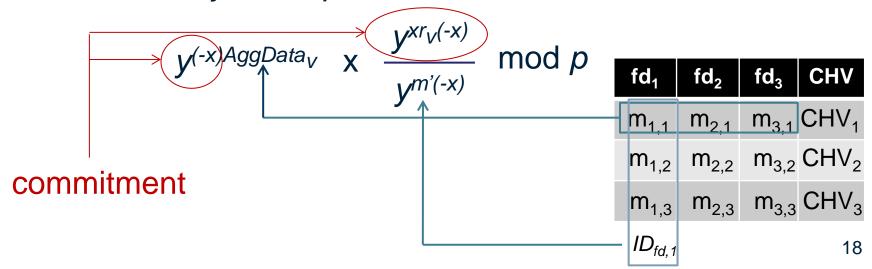
Delayed-Integrity-Verification

Integrity Verification

We need to solve this:

$$r'_i \mod p = (AggData_v + xr_v - m')x^{-1} \mod p$$

 But, essentially we want to compute CHA_HASH(m',r'), so we need y'' mod p, which is





Prototype Implementation

 Prototype was implemented using Java, and deployed on Raspberry Pi Model B+

CPU: 700 MHz Low Power ARM processor

Memory: 512 MB

Preliminary performance results

Device	Operation	Time (ms)
Controller	Chameleon Hashing	0.955955 (PC)
Field Device	Generation of Commitment	111.6 (Pi)
Back End	Integrity Verification	2.288591 (PC)
Field Device	Signature generation	5830 (Pi)





- Our scheme provides:
 - Data Integrity
 - Data Origin Authentication
 - Secure Data Aggregation
- Novel use of Chameleon Hashing and Signature other than its traditional usage, to detect misbehaviour of controllers or aggregators in ICS/SCADA.
- Future work:
 - Implement the protocol on real hardware or ICS platform.
 - Protocol can be generalized to be used in AMI, body sensor network, or any network with a hierarchical structure.





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